

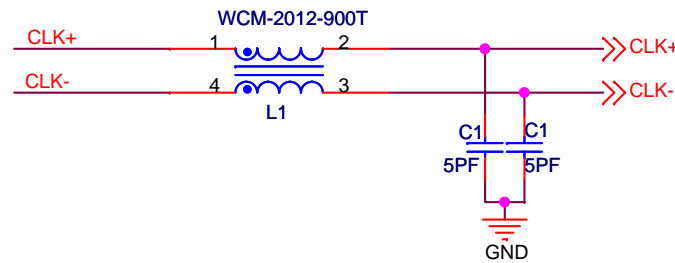
备注:

1、共模电感阻抗可根据实际的测试情况进行调整;

2、电容容值不能大于10pF, 否则会影响功能;

备注

3、共模电感在PCB布局时, 要尽量靠近主IC (始端) 放置;



一站式电磁兼容 (EMC) 方案供应商

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Title		
2285 差分时钟EMC设计标准电路		
Size A	Document Number Compon20110322-11	Rev 1.0
Date:	Sunday, March 27, 2011	Sheet 1 of 1